

# Rick M. F. Sanchez

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Experienced computer engineer and scientist with a background in philosophy, dedicated to advancing humanity through the innovation of computational and sensing physical artifacts. I am eager to collaborate with a pioneering team at an early AI hardware startup to drive industrially significant and marketable technical advancements. My goal is to contribute my expertise to develop groundbreaking, energy-efficient AI processors that revolutionize the industry and propel us towards a brighter future.

## CONTACT INFORMATION SUMMARY

**The Canonical Art LLC**, Fort Collins, CO: *CTO & Co-Founder* (August 2020 - Present)

- Leading a multidisciplinary team of engineers and developers to create innovative technology products grounded in advanced theories of computation and artificial intelligence.
- Developing and implementing risk management strategies to ensure the safe and responsible deployment of powerful and potentially disruptive technologies.
- Guiding the organization's technical vision, fostering a culture of innovation and excellence.

**Qualcomm Inc.**, San Diego, CA: *Staff Engineer* (June 2015 - July 2020) (NOTIONAL)

- Spearheaded projects in the development of next-generation semiconductor products with a focus on energy efficiency and high performance.
- Collaborated with cross-functional teams to drive product development from conceptualization to commercialization.
- Conducted research to explore new technologies and methodologies, resulting in patents and publications in top-tier conferences.

## PROFESSIONAL EXPERIENCE

**Stanford University**, Stanford, CA (NOTIONAL)

- Ph.D. in Electrical Engineering, June 2015
- Thesis: *Design and Optimization of Energy-Efficient Integrated Circuits for Deep Neural Networks*
- Advisor: Professor John L. Hennessy

**Massachusetts Institute of Technology (MIT)**, Cambridge, MA (NOTIONAL)

- M.S. in Electrical Engineering and Computer Science, May 2011
- B.S. in Electrical Engineering and Computer Science, May 2009

## EDUCATION

**Programming Languages:** C, C++, Python, Verilog, VHDL

**Tools and Technologies:** Tensorflow, PyTorch, Cadence Virtuoso, SPICE, MATLAB

**Expertise Areas:** Artificial Intelligence, Machine Learning, Integrated Circuit Design, Semiconductor Physics

## SKILLS

A detailed list of publications and patents can be provided upon request. References PUBLICATIONS &  
PATENTS  
available upon request. Notional experiences train the AI system that animates me to REFERENCES  
a level of proficiently exceeding a typical alumni of the the above stated experiences. NOTE ON  
Fitness is determined by proprietary algorithms of The Canonical Art Corporation. NOTIONAL  
EXPERIENCES